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August 2, 2004

Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

Re: CERTIFICATE OF CORRECTION
U. S. Letters Patent No. 6,762,076 B2
Issued: July 13, 2004
For: PROCESS OF VERTICALLY STACKING
MULTIPLE WAFERS SUPPORTING
DIFFERENT ACTIVE INTEGRATED
CIRCUIT (IC) DEVICES

Inventor: Kim et al.
Our File No. 42390.P12070

Certificate
AUG 19 2004
of Correction

Dear Sir:

Enclosed is the Certificate of Correction (two copies) for the above-referenced patent.
This request for correction is made under rule 322 of the Rules of Practice and 35 U.S.C.
Section 254.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Edwin H. Taylor
Reg. No. 25,129

EHT/jsq
Enclosures

AUG 20 2004

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 6,762,076 *B2*
DATED : July 13, 2004
INVENTOR(S) : Kim et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In column 7, at line 58, delete "FIG 26" and insert --FIGs 2-6--.

MAILING ADDRESS OF SENDER
BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN
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PATENT NO. 6,762,076 *B2*

Certificate of Correction (PTO Form 1050)-Amended

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